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05/05/2004

**MARSHALL, GERSTEIN & BORUN LLP
6300 SEARS TOWER
233 S. WACKER DRIVE
CHICAGO, IL 60606**



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James R. Zeller (Reg. 28,491)	(Depositor's name)
<i>[Signature]</i>	(Signature)
July 8, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/616,715	07/10/2003	Byung Soo Park	29936/39430	2630

TITLE OF INVENTION: METHOD OF FORMING A SELECT LINE IN A NAND TYPE FLASH MEMORY DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/05/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
BOOTH, RICHARD A	2812	438-264000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Marshall, Gerstein &
2 Borun LLP
3 _____

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hynix Semiconductor Inc.

Kyungki-do, Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ Advance Order - # of Copies 3

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07/13/2004 HLE444 00000059 10616715

01 FC:1501	1330.00 OP
02 FC:1504	300.00 OP
03 FC:8001	9.00 OP

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